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Jeffery D. Frazier

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EXAMINER

OLSEN, KAJ K

ART UNIT

PAPER NUMBER

1795

MAIL DATE

DELIVERY MODE

02/04/2009

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/800,388	Applicant(s) FRAZIER, JEFFERY D.	
	Examiner KAJ K. OLSEN	Art Unit 1795	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 August 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 30-45, 49, 55 and 56 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 30-45, 49, 55 and 56 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Specification

1. The objection to the specification has been withdrawn in view of the amendment.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 30-33, 38-41, 49, and 55 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of Arai and either Zanzucchi et al (USP 5,585,069) or Kercso et al (USP 6,132,685).
4. JP '061 discloses a system comprising a microdevice having a substrate (31, 32), a separation channel 33 formed in the substrate, a readable and rewritable memory 20 integrated into said substrate which stores information (inherently binary). JP '061 further discloses a means for causing at least one or analytes to migrate along the separation channel thereby separating said analyte. See fig. 1 and 2 and paragraphs 0003 and 0010. JP '061 does not explicitly disclose storing information about the analyte onto the chip nor does JP '061 explicitly identify biomolecules as being the analyte for the system. Arai establishes that the typical analyte for an electrophoretic experiment are biomolecules such as proteins and nucleic acids. See col. 1, ll. 13-15. Hence, it would have been obvious to one of ordinary skill in the art at the

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time the invention was being made to utilize the system of JP '061 for biomolecules, as suggested by Arai, because biomolecules are a typical analyte for electrophoretic systems. With respect to storing information about the biomolecules onto the memory, Zanzucchi suggests that information about the sample being analyzed by a microfluidic device should be included on the microfluidic device itself in the form of a barcode or other high density code. See col. 5, l. 59 - col. 6, l. 2. Kercso similarly teaches that information pertaining to the sample itself should be included on a plate holding a sample to facilitate sample management, and further suggests similar sample management can be utilized throughout all stages of the device, which includes microfluidic analysis. See fig. 5A and col. 8, ll. 24-38. Because both Zanzucchi and Kercso suggests that information about the sample constituent should be included with the microfluidic analysis for sample management purposes, it would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the memory of JP '061 to also contain information about the sample constituent so that the sample being analyzed by the microfluidic device is also suitably archived for sample management purposes. JP '061 even suggested that the contents of the included memory is not limited to the explicit examples given by JP '061. See paragraph 0012. Because Arai already identified biomolecules as being a suitable analyte for JP '061, information about the biomolecular containing sample (see Zanzucchi and Kercso) would read on the defined "character...of one of more biomolecules" giving the claim language its broadest reasonable interpretation.

5. With respect to the new limitation requiring the presence of a reader-writer unit configured to write said information to the readable and rewritable memory, JP '061 teaches that the device needs to contain information about the history of use of the chip (paragraph 0012). It

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would have been obvious to one of ordinary skill in the art to include a writer unit to interface with the chip so that this information can be updated (paragraph 0013). Furthermore, Zanzucchi and Kercso render obvious the inclusion of information concerning a biomolecule being analyzed (see above). Hence, a writer unit that can write information about what sample or analyte is present on the microchip would have also been obvious. Furthermore, for any of the information that has been written to the memory would have to also be readable as well as that is the whole purpose for the inclusion of the memory onto the microchip (JP '061 paragraphs 0012 and 0013).

6. With respect to the substrate composition and the use of an electric field, see paragraph 0003 of JP '061.

7. With respect to the memory being permanently affixed to the substrate, because JP '012 does not suggest removing the memory from the device and because one of the purposes of the memory is keep track of the use of the microfluidic device (paragraph 0012), one possessing ordinary skill in the art would recognize that the memory could be permanently affixed to the substrate such that it cannot be inadvertently removed thereby losing the history of the particular microfluidic device.

8. With respect to the type of memory, the EEPROM relied on by JP '061 would appear to read on either integrated circuit or thin film semiconductor memory. With respect to the amount of memory, finding the amount of memory that provides all the necessary storage capacity for the data to be stored, including the use of one megabyte, requires only routine skill in the art.

9. With respect to the use of a detector, see paragraphs 0003 and 0015 of JP '061.

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10. With respect to claim 49 (those limitations not discussed above), because the memory of JP '061 is meant to interface multiple analyzers (see paragraph 0013), it would have been obvious to one of ordinary skill in the art to place the machine-readable code necessary for reading or writing all of the data to the memory in the event that one of the analyzers utilized either does not have the appropriate code or does not have an updated version of the code. Placing this code on the memory itself would prevent different analyzers from accessing or transmitting data differently (or failing to do either).

11. With respect to claim 55 (those limitations not discussed above), storing information about the sample itself onto the memory (as rendered obvious by Zanzucchi and Kerso) would make the memory of JP '061 utilized for such a function a sample tracking device.

12. Claims 30-33, 38-41, 49, and 55 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of either Fujimiya et al (USP 6,017,434) or Simpson et al (USP 6,017,434).

13. JP '061 discloses a system comprising a microdevice having a substrate (31, 32), a separation channel 33 formed in the substrate, a readable and rewritable memory 20 integrated into said substrate which stores information (inherently binary). JP '061 further discloses a means for causing at least one or analytes to migrate along the separation channel thereby separating said analyte. See fig. 1 and 2 and paragraphs 0003 and 0010. JP '061 does not explicitly disclose storing information about the analyte onto the chip nor does JP '061 explicitly identify biomolecules as being the analyte for the system. However, Fujimiya teaches that a sample typically being analyzed by electrophoresis is DNA (a biomolecule) and further teaches that he measured sequencing data from the electrophoretic experiment should be stored on a data

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storage means for future reference. See col. 22, ll. 1-13. Simpson also teaches that electrophoresis is typically utilized for biomolecular analytes like DNA and also teaches that the measured sequence data for the DNA should be stored on a data storage means for future access and/or analysis. See col. 8, ll. 13-26. Because it was known that sequencing information from an electrophoretic experiment are typically stored in some memory means and because JP '061 was open ended about what information could be stored on the memory means (see "and so forth may be mentioned as information stored in EEPROM 20" in paragraph 0012), one possessing ordinary skill in the art would recognize that the memory of JP '061 could also store the measured sequence of the one or more biomolecules from the electrophoretic experiment, as suggested by Fujimiya and Simpson, to increase the utility of the already present memory means.

14. With respect to the new limitation requiring the presence of a reader-writer unit configured to write said information to the readable and rewritable memory, JP '061 teaches that the device needs to contain information about the history of use of the chip (paragraph 0012). It would have been obvious to one of ordinary skill in the art to include a writer unit to interface with the chip so that this information can be updated (paragraph 0013). Furthermore, Fujimiya and Simpson render obvious the inclusion of information concerning the data for the biomolecule being analyzed (see above). Hence, a writer unit that can write information about the actual sample data on the microchip would have also been obvious. Furthermore, for any of the information that has been written to the memory would have to also be readable as well as that is the whole purpose for the inclusion of the memory onto the microchip (JP '061 paragraphs 0012 and 0013).

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15. With respect to the various dependent claims, see the discussion of JP '061 in the preceding rejection.

16. With respect to claim 49 (those limitations not discussed above), because the memory of JP '061 is meant to interface multiple analyzers (see paragraph 0013), it would have been obvious to one of ordinary skill in the art to place the machine-readable code necessary for reading or writing all of the data to the memory in the event that one of the analyzers utilized either does not have the appropriate code or does not have an updated version of the code. Placing this code on the memory itself would prevent different analyzers from accessing or transmitting data differently (or failing to do either).

17. With respect to claim 55 (those limitations not discussed above), storing information about the sample itself onto the memory (as rendered obvious by Fujimiya and Simpson) would make the memory of JP '061 utilized for such a function a sample tracking device.

18. Claims 34-36 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of Arai and Zanzucchi or Kercso, or JP '061 in view of either Fujimiya or Simpson as applied to claim 30 above, and further in view of either Bjornson et al (USP 6,103,199) or Parce et al (USP 6,458,259) with or without the further teaching of Kroy et al (USP 5,252,294).

19. The references set forth all the limitations of claim 34, but did not explicitly recite that the means for causing the biomolecules to migrate comprised centrifugal force. However, both Bjornson and Parce teach that centrifugal forces are a conventional alternative to the use of electrosmotic and electrophoretic flow means for microchip devices. See Bjornson, col. 11, l. 55 - col. 12, l. 5 and Parce, col. 7, l. 63 - col. 8, l. 9. Because Bjornson and Parce demonstrate that centrifugal force means are conventional in microfluidic device art, it would have been obvious

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to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of centrifugal forces for the force means of JP '061 because the substitution of one known force for another requires only routine skill in the art.

20. With respect to claim 35, the use of centrifugal force inherently requires that the device be a spinning-disc. See for example Parce, col. 7, ll. 63-63-67.

21. With respect to claim 36, JP '061 does not explicitly suggest the use of optical memory as the storage means. However, Kroy teaches that optical memory was already a well known means for storing measurement data. See col. 5, ll. 45-59. It would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of optical memory, as suggested by Kroy, for the memory means of JP '061 because the substitution of one known memory means for another requires only routine skill in the art.

22. Claim 37 is rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of Arai and Zanzucchi or Kercso, or JP '061 in view of either Fujimiya or Simpson as applied to claim 30 above, and further in view of Bjornson.

23. The references set forth all the limitations of the claims, but did not explicitly disclose the presence of a plurality of non-intersecting separation channels. Bjornson teaches that it is conventional in the microfluidic art to include multiple non-intersecting separation channels on a single microchip so as to increase the amount of analysis that can be done with a single device. See fig. 5-7 and col. 8, ll. 13-40. It would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of Bjornson for the system of JP '061 in view of Arai and Zanzucchi or Kercso, or JP '061 in view of either Fujimiya or Simpson in order to increase the amount of analysis that can be done with a single device.

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24. Claim 42 is rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of Arai and Zanzucchi or Kercso, or JP '061 in view of either Fujimiya or Simpson as applied to claim 30 above, and further in view of Kaltenbach et al (USP 5,641,400).

25. The references set forth all the limitations of the claim, but did not explicitly recite the presence of a temperature control device to modulate the temperature of the substrate.

Kaltenbach teaches that microfluidic separation techniques can be affected by temperature and teaches the use of a temperature control device to regulate said temperature. See col. 3, ll. 9-38.

It would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of Kaltenbach for the system of JP '061 in view of Arai and Zanzucchi or Kercso, or JP '061 in view of either Fujimiya or Simpson in order to ensure that the temperature variation does not affect the quality of the separation being performed.

26. Claims 43-45 and 56 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of Arai, either Zanzucchi and/or Kercso, and Kaltenbach.

27. JP '061 discloses a system comprising a microdevice having a substrate (31, 32), a rewritable memory 20 integrated into said substrate which stores information (inherently binary).

See fig. 1 and 2 and paragraphs 0003 and 0010. JP '061 does not explicitly disclose storing information about the analyte onto the chip, the use of an array of polynucleotides as being the analyte for the system, or the use of a temperature control device. Arai establishes that the typical analyte for an electrophoretic experiment are biomolecules such as nucleic acids (i.e. polynucleotides). See col. 1, ll. 13-15. Hence, it would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the system of JP '061 for polynucleotides, as suggested by Arai, because polynucleotides are a typical analyte for

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electrophoretic systems. With respect to storing information about the biomolecules onto the memory, Zanzucchi suggests that information about the sample being analyzed by a microfluidic device should be included on the microfluidic device itself in the form of a barcode or other high density code. See col. 5, l. 59 - col. 6, l. 2. Kercso similarly teaches that information pertaining to the sample itself should be included on a plate holding a sample to facilitate sample management, and further suggests similar sample management can be utilized throughout all stages of the device, which includes microfluidic analysis. See fig. 5A and col. 8, ll. 24-38. Because both Zanzucchi and Kercso suggests that information about the sample constituent should be included with the microfluidic analysis for sample management purposes, it would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the memory of JP '061 to also contain information about the sample constituent so that the sample being analyzed by the microfluidic device is also suitably archived for sample management purposes. JP '061 even suggested that the contents of the included memory is not limited to the explicit examples given by JP '061. See paragraph 0012. Because Arai already identified biomolecules as being a suitable analyte for JP '061, information about the biomolecular containing sample (see Zanzucchi and Kercso) would read on the defined "character...of one of more biomolecules" giving the claim language its broadest reasonable interpretation. With respect to the temperature control, Kaltenbach teaches that microfluidic separation techniques can be affected by temperature and teaches the use of a temperature control device to regulate said temperature. See col. 3, ll. 9-38. It would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of

Kaltenbach for the system of JP '061 in view of Arai and Zanzucchi or Kercso, in order to ensure that the temperature variation do not affect the quality of the separation being performed.

28. With respect to the new limitation requiring the presence of a reader-writer unit configured to write said information to the readable and rewritable memory, JP '061 teaches that the device needs to contain information about the history of use of the chip (paragraph 0012). It would have been obvious to one of ordinary skill in the art to include a writer unit to interface with the chip so that this information can be updated (paragraph 0013). Furthermore, Zanzucchi and Kercso render obvious the inclusion of information concerning a biomolecule being analyzed (see above). Hence, a writer unit that can write information about what sample or analyte is present on the microchip would have also been obvious. Furthermore, for any of the information that has been written to the memory would have to also be readable as well as that is the whole purpose for the inclusion of the memory onto the microchip (JP '061 paragraphs 0012 and 0013).

29. With respect to the use of optical communication, see paragraph 0014 of JP '061.

30. With respect to claim 56 (those limitations not discussed above), storing information about the sample itself onto the memory (as rendered obvious by Zanzucchi and Kercso) would make the memory of JP '061 utilized for such a function a sample tracking device.

31. Claims 43-45 and 56 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP '061 in view of either Fujimiya or Simpson, and Kaltenbach.

32. JP '061 discloses a system comprising a microdevice having a substrate (31, 32), a rewritable memory 20 integrated into said substrate which stores information (inherently binary).

See fig. 1 and 2 and paragraphs 0003 and 0010. JP '061 does not explicitly disclose storing

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information about the analyte onto the chip, identify polynucleotides as being the analyte for the system, or disclose the use of a temperature control device. However, Fujimiya teaches that a sample typically being analyzed by electrophoresis is DNA (a polynucleotide) and further teaches that the measured sequencing data from the electrophoretic experiment should be stored on a data storage means for future reference. See col. 22, ll. 1-13. Simpson also teaches that electrophoresis is typically utilized for polynucleotides analytes like DNA and also teaches that the measured sequence data for the DNA should be stored on a data storage means for future access and/or analysis. See col. 8, ll. 13-26. Because it was known that sequencing information from an electrophoretic experiment are typically stored in some memory means and because JP '061 was open ended about what information could be stored on the memory means (see "and so forth may be mentioned as information stored in EEPROM 20" in paragraph 0012), one possessing ordinary skill in the art would recognize that the memory of JP '061 could also store the measured sequence of the one or more biomolecules from the electrophoretic experiment, as suggested by Fujimiya and Simpson, to increase the utility of the already present memory means. With respect to the temperature control, Kaltenbach teaches that microfluidic separation techniques can be affected by temperature and teaches the use of a temperature control device to regulate said temperature. See col. 3, ll. 9-38. It would have been obvious to one of ordinary skill in the art at the time the invention was being made to utilize the teaching of Kaltenbach for the system of JP '061 in view of Fujimiya or Simpson, in order to ensure that the temperature variation do not affect the quality of the separation being performed.

33. With respect to the new limitation requiring the presence of a reader-writer unit configured to write said information to the readable and rewritable memory, JP '061 teaches that

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the device needs to contain information about the history of use of the chip (paragraph 0012). It would have been obvious to one of ordinary skill in the art to include a writer unit to interface with the chip so that this information can be updated (paragraph 0013). Furthermore, Fujimiya and Simpson render obvious the inclusion of information concerning the data for the biomolecule being analyzed (see above). Hence, a writer unit that can write information about the actual sample data on the microchip would have also been obvious. Furthermore, for any of the information that has been written to the memory would have to also be readable as well as that is the whole purpose for the inclusion of the memory onto the microchip (JP '061 paragraphs 0012 and 0013).

34. With respect to the use of optical communication, see paragraph 0014 of JP '061.

35. With respect to claim 56 (those limitations not discussed above), storing information about the sample itself onto the memory (as rendered obvious by Fujimiya or Simpson) would make the memory of JP '061 utilized for such a function a sample tracking device.

Double Patenting

36. The previous double patenting rejection has been withdrawn in view of the filed terminal disclaimer, which has been approved.

Response to Arguments

37. Applicant's arguments filed 8-19-2008 have been fully considered but they are not persuasive. With respect to the rejection of JP '061 ("Hiroshi" in the arguments) in view of Zanzucchi or Kercso, applicant urges that the barcode and barcode readers of Zanzucchi and

Kercso are not devices that can be written to and retrieved from. Although the examiner might agree with this conclusion, this argument indicates that the applicant might have misunderstood the rejection in question. The examiner is not urging that one take the barcodes of either Zanzucchi or Kercso and include it with the device of JP '061, but rather one include the type of information that Zanzucchi or Kercso placed on a barcode into the memory of JP '061. See the sentence beginning "Because" on p. 4 of the 5/27/2008 office action. It would appear to the examiner that the use of memory of JP '061 is an improvement over the known prior art use of things such as barcodes or other external readable media where "model names are inscribed on respective chips to make them identifiable and the information is recorded externally" (paragraph 0006). What Zanzucchi and Kercso set forth is that information about the character or sequence of one of more analytes (biomolecules according to Arai) can also be externally recorded onto a microfluidic chip, and one of ordinary skill in the art would recognize that that information could also be included into the memory on the microfluidic chip of JP '061, especially when JP '061 was open-ended about what could be included in the memory (paragraph 0012).

38. With respect to the rejection of JP '061 in view of Fujimiya or Simpson, applicant urges that neither of the teachings Fujimiya or Simpson teach or suggest readable or rewriteable memory or a reader-writer unit. This is unpersuasive for a couple of reasons. First, JP '061 already taught that the memory should be continually updated to record the history of use of the device (paragraph 0012), which means that the memory of JP '061 would have to be rewriteable. Furthermore, if the memory of JP '061 were not also readable (i.e. the information contained within it be accessible), then there would be no purpose for the memory in the first place. Hence, the primary teaching of JP '061 already renders obvious the use of a reader-writer unit

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configured to write information to the readable and rewriteable memory and it irrelevant whether Fujimiya and Simpson also teach doing do. The rejection in question is not whether it would have been obvious to utilize the memory means of Fujimiya or Simpson on the microfluidic chip as it appears the applicant is framing the issue, but rather would it have been obvious to include the data that Fujimiya and Simpson disclose storing onto to the already present memory of JP '061. Second, it would have been obvious for Fujimiya and Simpson to also use a readable and rewriteable memory as well as utilize a reader-writer unit for reading and rewriting the memory. In particular, Fujimiya teaches that the electrophoresis data could be stored on a magnetic disk as an example (col. 22, ll. 6-13) and a magnetic disk is a known readable and rewriteable memory. Although Simpson does not specify a particular means of memory, a readable and rewriteable memory would have also been an obvious choice of memory as well (see both JP '061 and Fujimiya for examples of suitable rewriteable memory). Moreover, because both Fujimiya and Simpson are being utilized to store measurement data for later analysis, the use of a reader-writer unit that is capable of reading and writing (or rewriting) the data to the disk would also have been obvious as well. Using a writer for rewriting the memory means the memory in question can be reused over and over again, and the use of a reader would allow one to access the measured data for further analysis. If the data is not accessible by a reader unit, then there is no purpose for the memory.

39. Applicant's arguments concerning the use of Bjornson, Parce, Kroy, and Kaltenbach all appear to rely on the applicant's earlier perceived failure of the rejections relying on Zanzucchi, Kercso, Fujimiya, or Simpson. Because these earlier arguments were not persuasive as discussed above, these further arguments were similarly unpersuasive.

Conclusion

40. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KAJ K. OLSEN whose telephone number is (571)272-1344. The examiner can normally be reached on M-F 5:30-2:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nam X. Nguyen can be reached on 571-272-1342. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Kaj K Olsen/
Primary Examiner, Art Unit 1795
February 4, 2009